


Appendix A
Verification of Translation

US Patent Application No. 10/585,016

Title of the Invention: AN OVERLAPPING COMMAND AT ONE STAGE
SUBMITTING METHOD OF DYNAMIC CYCLE PIPELINE (As Amended)

I, Xinghao Wang, whose full office address is Hamre, Schumann, Mueller & Lason,, P.C., 45 South Seventh Street, Suite 2700, Minneapolis, MN 55408, am the translator of the document attached and I state that the following document is a true translation of the paragraphs identified in the Document Submitted to the best of my knowledge and belief of the relevant portions of PCT publication WO2005/064481 (filed on: 6/22/2004, which claims priority to CN 200310112972.1 (filed on: 12/29/2003).

Signature of the translator

A handwritten signature in black ink, appearing to be 'Xinghao Wang', written over a horizontal line.

Xinghao Wang

Date: 1/26/2012

Document Submitted: English Translation of the paragraph from lines 10 to 15 on page 4 of PCT publication WO2005/064481 is the following:

The so called overlapping submitting method means inserting a new command in the last cycle of certain command before its exiting the pipeline, as is shown in Fig. 5, Command G is inserted in the last cycle of Command A before Command A exiting, so as to avoid the appearance of bubbles. In order to carry out the above method, it is required to provide a command exiting signal as early as possible during the pipeline period immediately before the last cycle of the exiting command; and it shall ascertain that there is no command relevance between the newly inserted command and the exiting command; in addition, between the inserted and exiting commands, the possible conflict of command fields formed by registers (referring to the current registers) shall be resolved.

Document Submitted: English Translation of the paragraph from lines 9 to 15 on page 5 of PCT publication WO2005/064481 is the following:

In Step 150, in order to ascertain that the command interpreter can promptly determines the timing of inserting the command and to help the command interpreter determine the timing of inserting the command, it is required to provide an exiting signal indicating that a command is exiting the pipeline as early as possible in the pipeline period that is immediately before the last cycle of the exiting command. The timing of releasing the pipeline exiting signal is fixed, which in this embodiment is two stages prior to when the new command is inserted into the pipeline stage. Taking Fig. 5 as an example, the new Command G is inserted at No. 1 pipeline stage, thus the pipeline exiting signal indicating that the old Command A is exiting is released at the fifth stage of the pipeline period immediately before the last cycle of the exiting Command A, and when the exiting Command A flows to the sixth stage of the pipeline, a command adding judgment logic determines whether or not to insert the new Command G.